

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	34	((erasure pointer) near5 (set\$3 or command or generat\$3 or memory or stor\$3)) with (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 17:28
L2	71	((erasure pointer) near5 (set\$3 or command or generat\$3 or memory or stor\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 17:27
L3	12626	(erasure near5 (set\$3 or command or generat\$3 or memory or stor\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:26
L4	118	erasure pointer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:29
L5	4106	(I2 or I3 or I4) same (switch switching or inhibit\$3 or prevent\$3 or block\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:30
L6	58	(I2 or I3 or I4) with (switch switching or inhibit\$3 or prevent\$3 or block\$3) with (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:32
L7	8	(I2 or I3 or I4) with (switch switching or inhibit\$3 or prevent\$3) with (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:35
L8	35	(I2 or I3 or I4) same (switch switching or inhibit\$3 or prevent\$3) same (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:39

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L9	9	(l2 or l3 or l4) same (switch switching or inhibit\$3) same (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:56
L10	14586	(switch switching or inhibit\$3) near5 (pointer or command or information or rewritten or rewrit\$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 16:58
L11	150	erasure with l10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 17:27
L12	5	l11 with (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 17:28
L13	7	l11 same (ecc or (error correct\$3) or edc or (error detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	ON	2006/09/12 17:28

Titles of most frequently occurring classifications of patents returned
from a search of 10743551 on Sep 12 , 2006

- 11 714/755 (9 OR, 2 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/755Double encoding codes (e.g., product, concatenated)
- 10 714/784 (5 OR, 5 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/781Code based on generator polynomial
 - 714/784Reed-Solomon code
- 8 714/769 (2 OR, 6 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/763Memory access
 - 714/769Dynamic data storage
- 7 714/756 (4 OR, 3 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/755Double encoding codes (e.g., product, concatenated)
 - 714/756Cross-interleave Reed-Solomon code (CIRC)
- 7 714/765 (4 OR, 3 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/763Memory access
 - 714/765Error pointer
- 6 714/785 (2 OR, 4 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/781Code based on generator polynomial
 - 714/785Syndrome computed
- 4 714/758 (2 OR, 2 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/758Error correcting code with additional error detection
- code (e.g., cyclic redundancy character, parity)
- 4 714/761 (0 OR, 4 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction

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- 714/752 ...Forward correction by block code
714/761Random and burst error correction
- 3 714/763 (0 OR, 3 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/746 ..Digital data error correction
714/752 ...Forward correction by block code
714/763Memory access
- 3 714/766 (2 OR, 1 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/746 ..Digital data error correction
714/752 ...Forward correction by block code
714/763Memory access
714/766Check bits stored in separate area of memory
- 3 714/774 (2 OR, 1 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/746 ..Digital data error correction
714/752 ...Forward correction by block code
714/774Adaptive error-correcting capability
- 3 714/757 (2 OR, 1 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/746 ..Digital data error correction
714/752 ...Forward correction by block code
714/757Parallel generation of check bits
- 2 714/752 (2 OR, 0 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/746 ..Digital data error correction
714/752 ...Forward correction by block code
- 2 714/711 (2 OR, 0 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/710 ..Replacement of memory spare location, portion, or segment
714/711 ...Spare row or column
- 2 714/701 (0 OR, 2 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/701 ...Data formatting to improve error detection correction
capability
- 2 365/200 (1 OR, 1 XR)
Class 365 STATIC INFORMATION STORAGE AND RETRIEVAL
365/189.01 .READ/WRITE CIRCUIT
365/200 ...Bad bit
- 2 714/775 (1 OR, 1 XR)
Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
714/699 .PULSE OR DATA ERROR HANDLING
714/746 ..Digital data error correction
714/752 ...Forward correction by block code
714/775Synchronization
- 2 714/781 (1 OR, 1 XR)

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- Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/781Code based on generator polynomial
- 2 714/762 (0 OR, 2 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/762Burst error correction
- 2 341/94 (0 OR, 2 XR)
 - Class 341 CODED DATA GENERATION OR CONVERSION
 - 341/50 .DIGITAL CODE TO DIGITAL CODE CONVERTERS
 - 341/94 ..With error detection or correction
- 2 714/797 (1 OR, 1 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/797 ...Majority decision/voter circuit
- 2 714/772 (0 OR, 2 XR)
 - Class 714 ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY
 - 714/699 .PULSE OR DATA ERROR HANDLING
 - 714/746 ..Digital data error correction
 - 714/752 ...Forward correction by block code
 - 714/763Memory access
 - 714/772Code word parallel access